

1.5 Amp, 12V Step-Up/Step-Down
Integrated Switching Regulator



SLTS139

(Revised 2/7/2001)



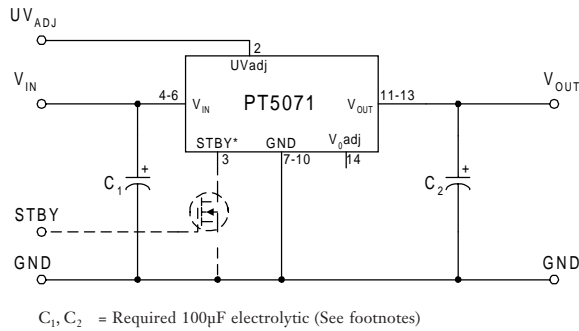
Features

- Single-Device:
+12V Output, 7-16V Input
- 84% Efficiency
- 14-Pin Excalibur™ Package
- Output Current Limit
- Adjustable Output Voltage
- Adjustable Undervoltage Lockout
- Solderable Copper Case

Description

The PT5071 is a 1.5-ampere rated step-up/step-down Integrated Switching Regulator (ISR) that provides a tightly regulated 12V output voltage from a 7V to 16V variable input source. This high-performance ISR has applications in systems where the input voltage straddles the desired 12V output. The regulator has an adjustable output voltage and input start-up threshold, and a standby function for power conservation.

Standard Application



Pin-Out Information

Pin	Function
1	N/C
2	UVLO Adj
3	STBY*
4	V _{in}
5	V _{in}
6	V _{in}
7	GND
8	GND
9	GND
10	GND
11	V _{out}
12	V _{out}
13	V _{out}
14	V _{out} Adjust

Ordering Information

PT5071□ = +12 Volts

PT Series Suffix (PT1234X)

Case/Pin Configuration

Vertical Through-Hole	N
Horizontal Through-Hole	A
Horizontal Surface Mount	C

(For dimensions and PC board layout, see Package Styles 1360 and 1370.)

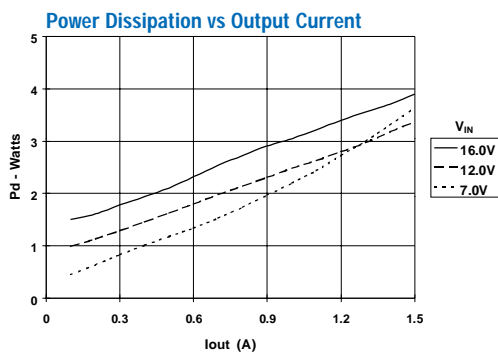
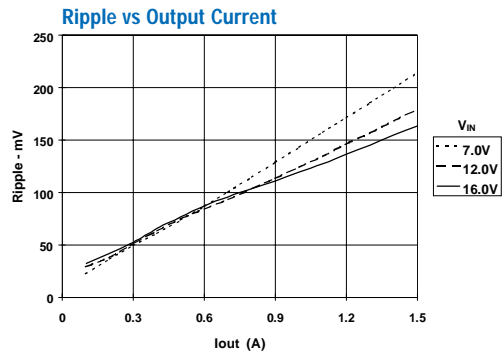
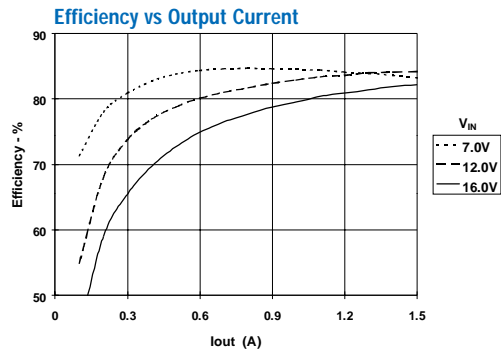
For Inhibit pin:
Open = output enabled
Ground = output disabled

Specifications

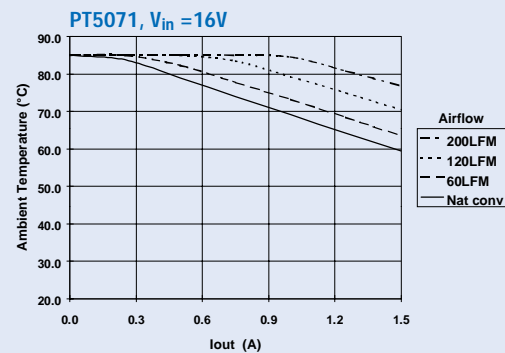
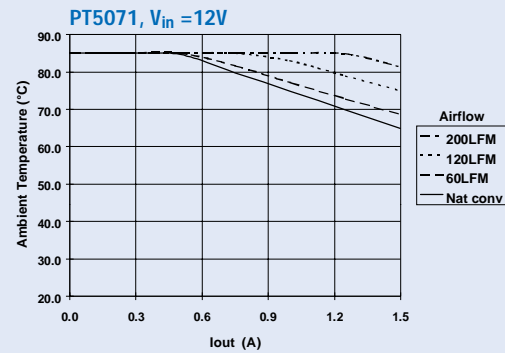
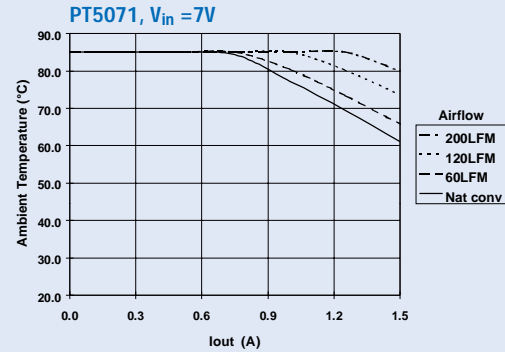
Characteristics (T _a = 25°C unless noted)	Symbols	Conditions	PT5071			Units
			Min	Typ	Max	
Output Current	I _o	Over V _{in} Range	0.1 (1)	—	1.5	A
Current Limit	I _{lim}	V _{in} = 12V	—	4.0	—	A
Input Voltage Range	V _{in}	0.1A ≤ I _o ≤ I _o max	7.0	—	16.0	V
Output Voltage Tolerance	ΔV _o	V _{in} = 12V, I _o = I _o max −40°C ≤ T _a ≤ +85°C	—	±1.0	—	%
Output Voltage Adjust Range	V _o adj	—	10	—	15	V
Line Regulation	Reg _{line}	Over V _{in} Range, I _o = I _o max	—	±0.5	—	%
Load Regulation	Reg _{load}	V _{in} = 12V, 0.1 ≤ I _o ≤ I _o max	—	±0.5	—	%
V _o Ripple/Noise	V _n	V _{in} = 12V, I _o = I _o max	—	±2.0	±3.0	%
Transient Response with C ₂ = 100 μ F	t _{tr} V _{os}	Load step from 50% to 100% I _o max, V _{in} = 12V V _o over/undershoot	— —	200 1.0	—	μ Sec %V _o
Efficiency	η	V _{in} = 12V, V _o = 12V, I _o = 1.5A	—	83	—	%
Switching Frequency	f _o	Over V _{in} Range 0.1A ≤ I _o ≤ I _o max	—	550	—	kHz
Absolute Maximum Operating Temperature Range	T _a	Over V _{in} range	−40 (2)	—	+85 (3)	°C
Storage Temperature	T _s	—	−40	—	+125	°C
Mechanical Shock	—	Per Mil-STD-883D, Method 2002.3, 1 msec, Half Sine, mounted to a fixture	—	TBD	—	G's
Mechanical Vibration	—	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, Soldered in a PC board	—	TBD	—	G's
Weight	—	—	—	25	—	grams

- Notes:**
1. The regulator will operate down to no load with reduced specifications.
 2. For operating temperatures below 0°C, it is recommended that tantalum capacitors be used at both the input and output.
 3. See SOA curves, or contact the factory for derating guidelines.

Input/Output Capacitors: The PT5071 regulator requires a 100 μ F electrolytic capacitor at the input and output for proper operation in all applications. The ESR (equivalent series resistance) of both capacitors must be less than 250m Ω @100kHz. In addition, C₁ and C₂ must be rated to a minimum of 300mA rms ripple current.

PT5071 Performance, $V_o = 12V$ (See Note A)

Safe Operating Area Curves (See Note B)



Note A: All Characteristic data in the above graphs has been developed from actual products tested at 25°C. This data is considered typical data for the ISR.

Note B: SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

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